Thottathil (H), Neriamangalam
Ernakulam
Pin: 686693
Mob:9400528681
Email : darsanavijay88@gmail.com

Darsana Vijay

Date of Birth: 20-05-1988
Marital Status: Married

Career Objective:

Seeking a position that utilizes my experiences, skills and abilities in academics and research to mature myself as an administrator in education industry.

Academic Profile

Qualification	Year	% of marks	Institute/ University
UGC-NET for Assistant Professor Electronic Science	2022	NA	UGC
M.Tech. Embedded Systems	2011	80.70	Karunya University, Coimbatore
B.Tech. Electronics and Communication Engineering	2009	72.00	From Mahatma Gandhi University through Sree Narayana Gurukulam College of Engineering,Kadayiruppu
Plus Two (CBSE)	2005	72.40	Jawahar Navodaya Vidyalaya, Neriamangalam
Tenth (CBSE)	2003	87.40	Jawahar Navodaya Vidyalaya, Neriamangalam

Work Experience: Academics

Profile	Institution	Course	Duration
Assistant. Professor (on contract **-** Electronics and Communication)	**Cochin University of Science and Technology, Kalamassery**	**M.Tech. and B.Tech.** (Dept of Instrumentation)	27th Sept 2018 till 10th Dec 2021
Assistant. Professor (Electronics and Communication)	**Ilahia College of Engineering and Technology**, Mulavoor, **Muvattupuzha**	**M.Tech. and B.Tech.** (Dept of Electronics and Communication)	10th Aug 2011 till 20th May 2017

Academic Job Profile

Class mentor

Teacher in charge

- Setting up VLSI and Embedded Systems Lab for 8th semester B.Tech,
- Setting up of Basic electronics Engineering Workshop for first year B.Tech
- Setting up of Digital Electronics Lab for 3rd semester B.Tech.

Programme Coordinator

- National Seminar ASCII2019 at CUSAT, Kalamassery
- Technical session of National Conference at ICET,Mulavoor
- Project exhibition as part of Technical fest at ICET, Mulavoor
- Fun zone as part of Technical fest at ICET, Mulavoor
- Seminar on Introduction to Digital Signal Processing held at ICET
- Workshop on PCB Design for B.Tech students
- State level training programme for N-act members of NSS
- One Day Workshop on Pspice conducted by LEONTECH, Palarivatom, Cochin
- Faculty Development Programme on Signal Processing
- Two days workshop on SPSS

Syllabus Setter

- Advanced Digital Signal Processing for 1st semester M.Tech, Applied Electronics, Kerala Technical University.

Question Paper Setter

- Power Electronics for 4th semester B Tech, Instrumentation Technology, CUSAT (2019, 2020 & 2022)
- Analog Electronics for 2nd semester B Tech, Instrumentation Technology, CUSAT (2019, 2020 & 2022)
- Basic Electronics for 1st semester B Tech, Instrumentation Technology, CUSAT (2019, 2020 & 2022)
- PC Based Instrumentation for 2nd semester M Tech, Instrumentation Technology, CUSAT (2019, 2020 & 2022)
- Medical Imaging Systems Using Ionizing Radiations for 3rd semester MSc in Biomedical Instrumentation, MG University (2020)

Subjects Handled

- Microprocessor and Microcontroller
- Analog Circuits
- Power Electronics
- Advanced Digital Signal Processing
- Digital Signal Processing
- Basic Electronics Engineering
- Embedded systems
- Medical Electronics
- C Programming
- VLSI & Embedded Systems Lab.
- Electronics Design and Automation Lab
- C Programming Lab
- Analog Circuits Lab
- Systems Lab

Technical Skills:

- **Programming Languages** : C, C++, Verilog, VHDL
- **Softwares** : LabVIEW, Matlab, Xilinx, ModelSim, Express PCB, MPLAB IDE, Proteus, Pspice

RESEARCH PAPERS PUBLISHED

Journal : International Journal of Engineering And Computer Science

ISSN : 2319-7242, Volume 3 Issue 9, September 2014 Page No. 8377-8381

Impact Factor: 3.093

Title : Improving Edge Based Color Constancy for Multiple Light Sources

Journal : International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering,

ISSN (Print) : 2320 – 3765, Volume 3, Special Issue 5, Dec 2014

Impact Factor: 5.016

Title : SPECULAR EDGE WEIGHTING FOR LOCAL COLOR CORRECTION OF IMAGES

Research Papers Presented In Conferences:

Conference : **2015 International Conference on Recent Advancements & Challenges in Engineering**

Organiser : Jai Bharath College of Management and Engineering Technology,Perumbavoor,Ernakulam

Date : 03-10-2015

Title : An Adaptive Approach for Image Enhancement and Naturalness Preservation

Conference : **International Conference on Emerging Technology Trends (ICETT-2011)**

Organiser : Baselios Mathews II College of Engineering, Sasthamcotta, Kollam, Kerala

Date : 25-03-2011

Title : Maximum Power Point Tracking Algorithm with Improved Tracking Speed for Solar Panel

Conference : **National Conference on Trends in Electronics, Instrumentation, Embedded System & Automation**

Organiser : Department of Electronics & Instrumentation, Karunya University, Coimbatore

Date : 26-03-2011

Title : A Novel Approach for Maximum Power Point Tracking in Photovoltaic System

Seminars Attended

Seminar: **FPGAs for Signal Processing and Embedded Applications**

Date: **04-03-2010 to 06 -03 -2010**

Organiser: **M.S.Ramaiah School of Advanced Studies**

Training and Workshop Undergone:

Training Programme: Short term training programme on Speech Processing Sponsored by ISTE

Date: **14-12-2015 to18-12-2015**

Organiser: **Department of Electronics and Communication Engineering, Viswajyothi College of Engineering and Technology,Vazhakulam, Ernakulam**

Training Programme: Faculty Development Programme on "Instructional Design & Delivery System

Date: **02-02-2012 to 04 -02 -2012**

Organiser: **Toc H Institute of Science & Tech, Arakunnam**

Training Programme: Industrial Training - PIC Microcontrollers

Date: **18-12-2008 to 02 -01 -2009**

Organiser: **E-Smart Technologies conducted at Sree Narayana Gurukulam College of Engineering, Kadayiruppu**

Workshop: Three Day Workshop on System Design Using LabVIEW

Date: **30-10-2019 to 01-11-2019**

Organiser: **Department of Instrumentation in Association with National Instruments, Banglore**

Workshop: Workshop on Electronic CAD conducted by Rackbrains Technologies, Kochi

Date: **27-10-2015**

Organiser: **Ilahia College of Engineering and Technology,Muvattupuzha, Ernakulam**

Workshop: **Emerging Trends in VLSI Design and Embedded Systems**

Date: **31-07-2015 to 31 -08 -2015**

Organiser: **Dept. of ECE, M A C E, Kothamangalam Sponsored by Kerala State Council for Science, Technology and Environment**

Workshop: **FPGAs and Interfaces for Signal Processing**

Date: **21-01-2011 to 22 -01 -2011**

Organiser: **Department of Electronics and Instrumentation Engineering, Karunya University, Coimbatore**

Workshop: VHDL

Date: **14-09-2006 to 15 -09 -2006**

Organiser: **IEEE student Branch Sree Narayana**

                        Gurukulam College of Engineering, Kadayiruppu

Project Undertaken As A Part Of Academic Curriculum

1. **A NOVEL APPROACH FOR MAXIMUM POWER POINT TRACKING IN PHOTOVOLTAIC SYSTEM**

2. **ROUTE TRACKING-DROMOGRAPHY**

Referrence:

1) **Dr. Johney Isaac**

    HOD & Associate Professor

    Department of Instrumentation

    Cochin University of Science and Technology, Kalamassery

    (M) 8281535741, (e-mail)johney@cusat.ac.in

2) **Dr. Pankaj Sagar**

    Assistant Professor

    Department of Instrumentation

    Cochin University of Science and Technology, Kalamassery

    (M) 9611366896, (e-mail)pankajs@cusat.ac.in

3) **Mr. Ashith S**

    Placement Officer

    Career Guidance and Placement Unit

    Muthoot Institute of Technology and Science, Varikoli, Ernakulam-682308

    (M) 9995883651, (e-mail) ashiths@mgits.ac.in

Declaration:

        I hereby declare that the above-mentioned information is correct up to my knowledge and I bear the responsibility for the correctness of the above-mentioned particulars.

Place: Neriamangalam

Date: 16-06-2022